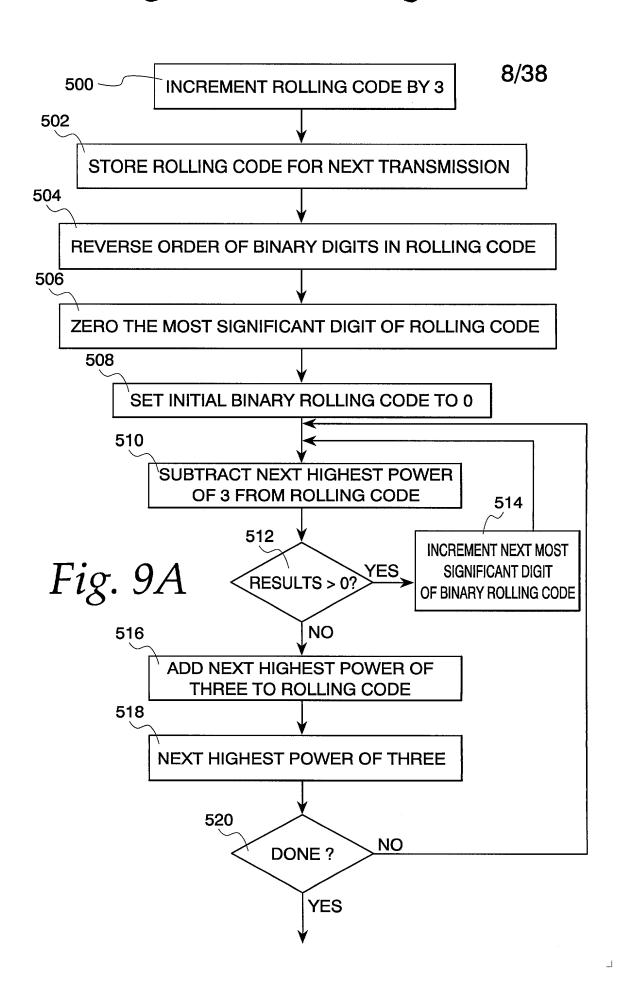


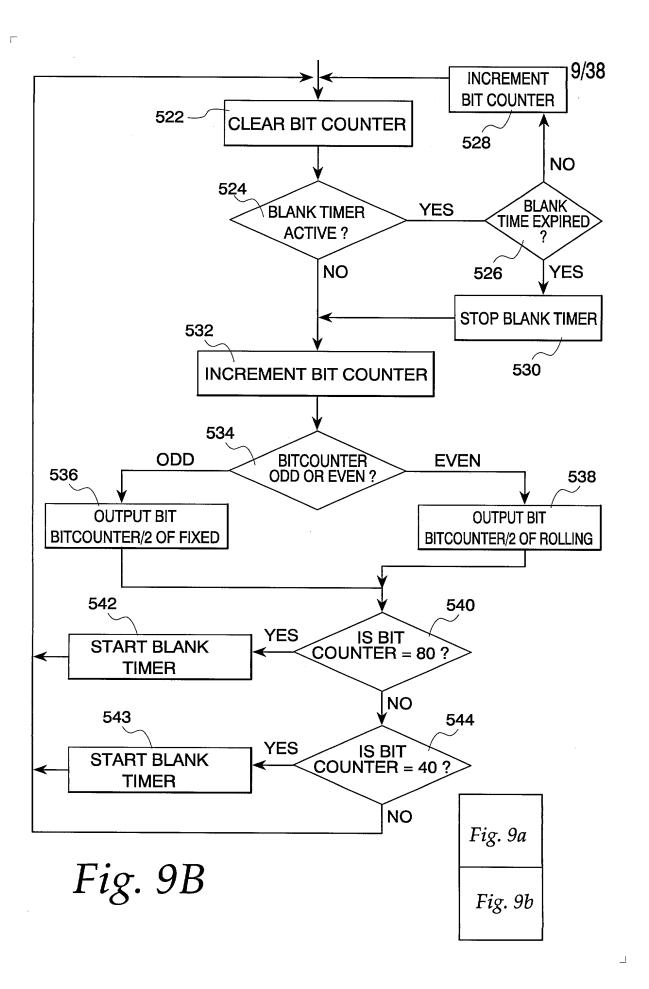
B2 B3 B4 B5 B6 B7 B8 B9 B10 BLANK 39	B12 B13 B14 B15 B16 B17 B18 B19 B20 BLANK 37 $Fig.\ 13$
SYNC 1.0	SYNC3 B11 B12 B13

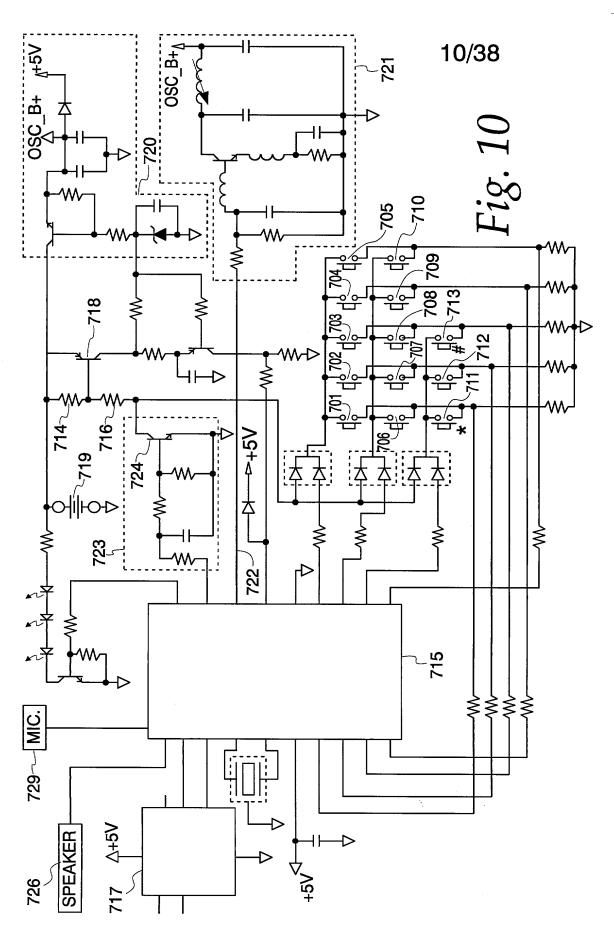
A CODE BY A CODE A CODE B11 A CODE B12 A CODE B13 A CODE B15 A CODE B16 ID BIT 1 ID BIT 0 SWITCH ID

7/38 730 731 732 733 734 735 736 737 738 739
ACODE BY A CODE BY A CODE

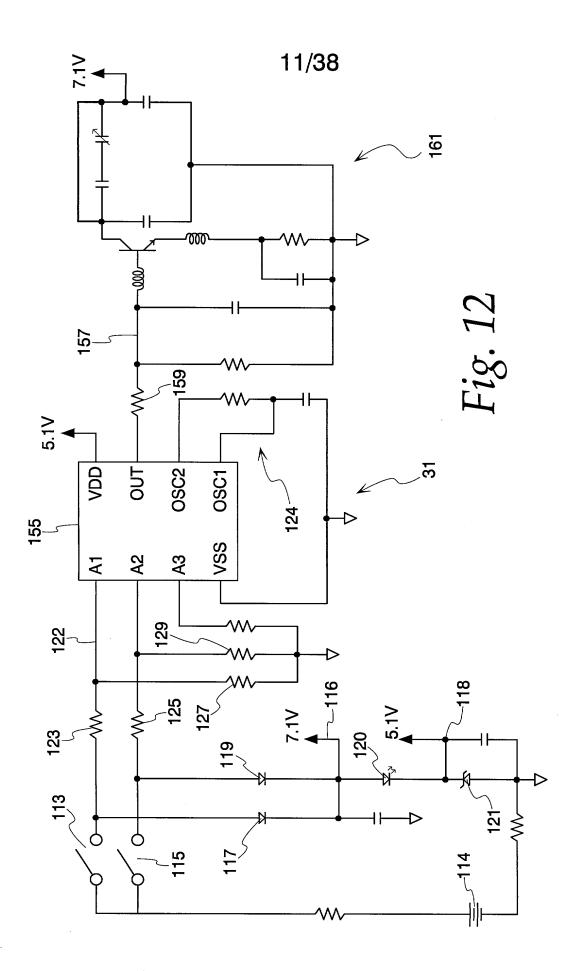
| MSD | 742 | 743 | 745 | 746 | ACODE B19 | ACODE B20 | ACODE B19 | ACODE B19 | ACODE B20 | ACODE B19 | ACODE B19



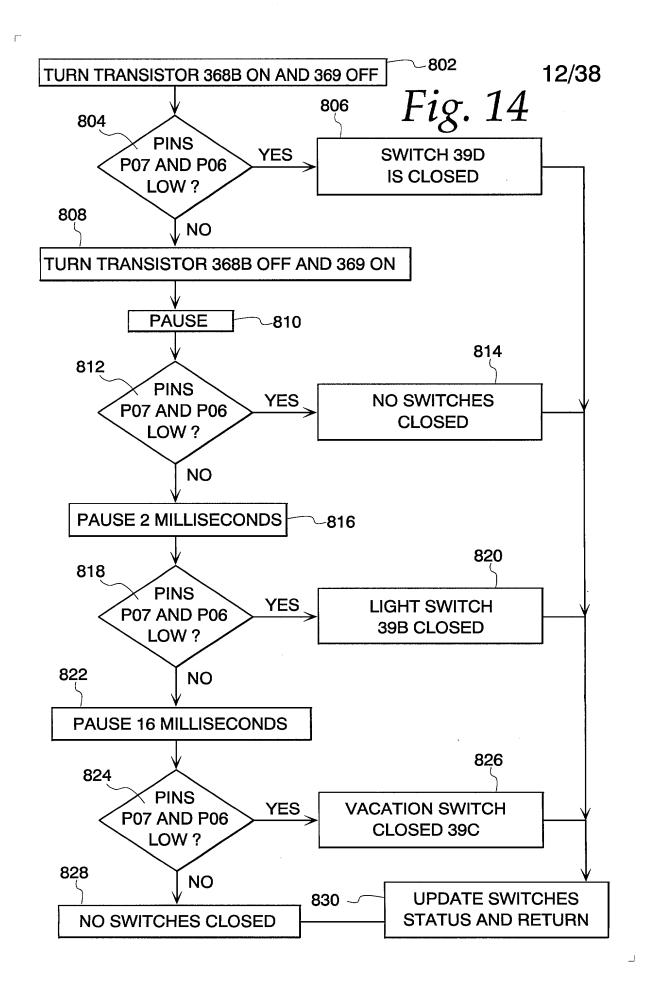




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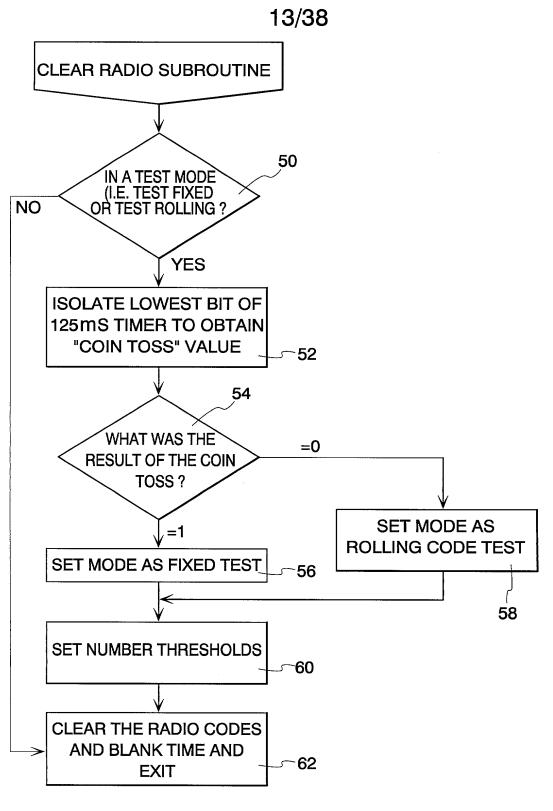
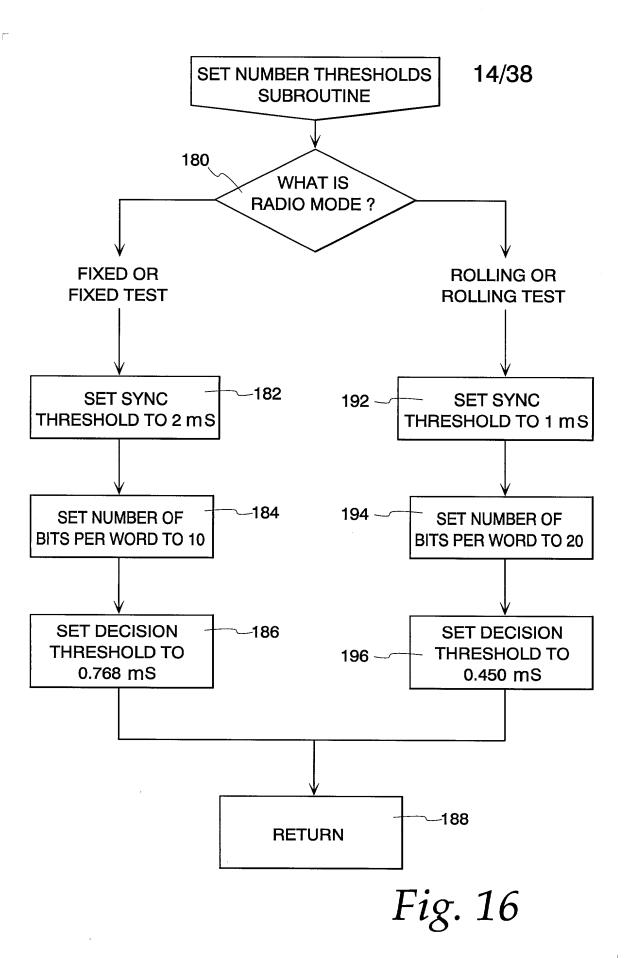
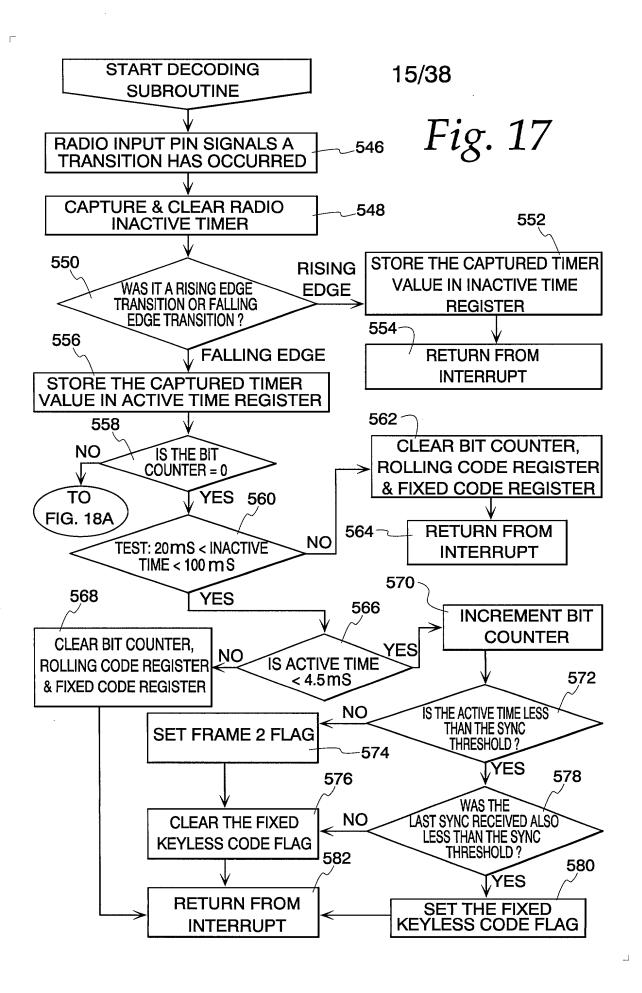
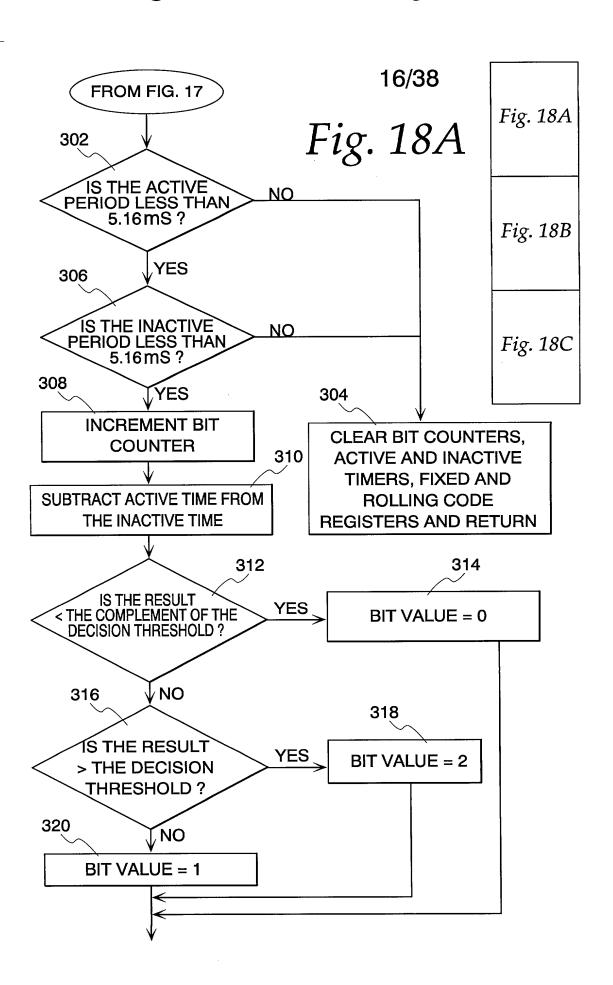


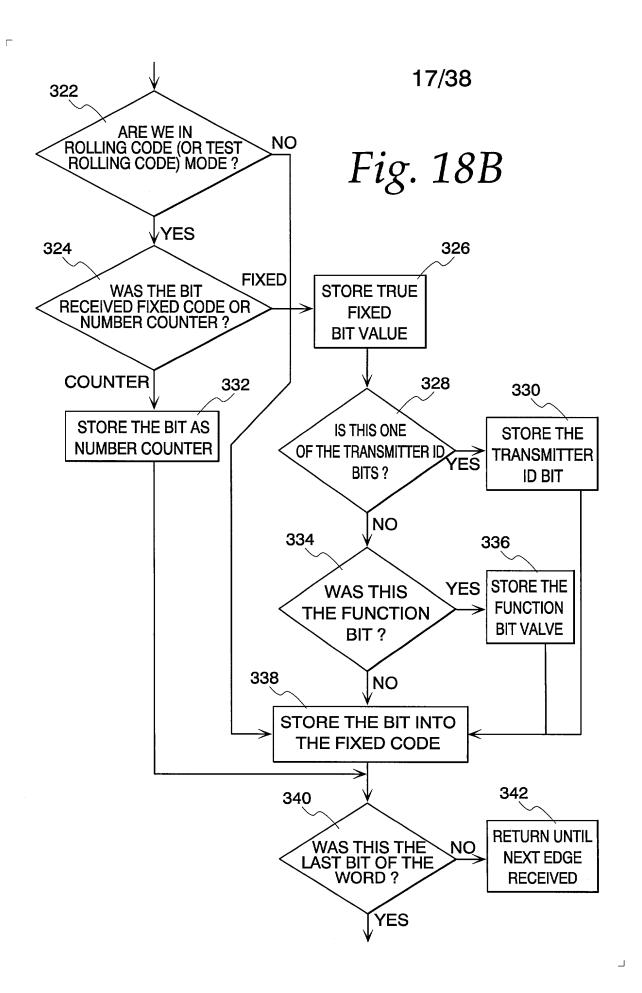
Fig. 15

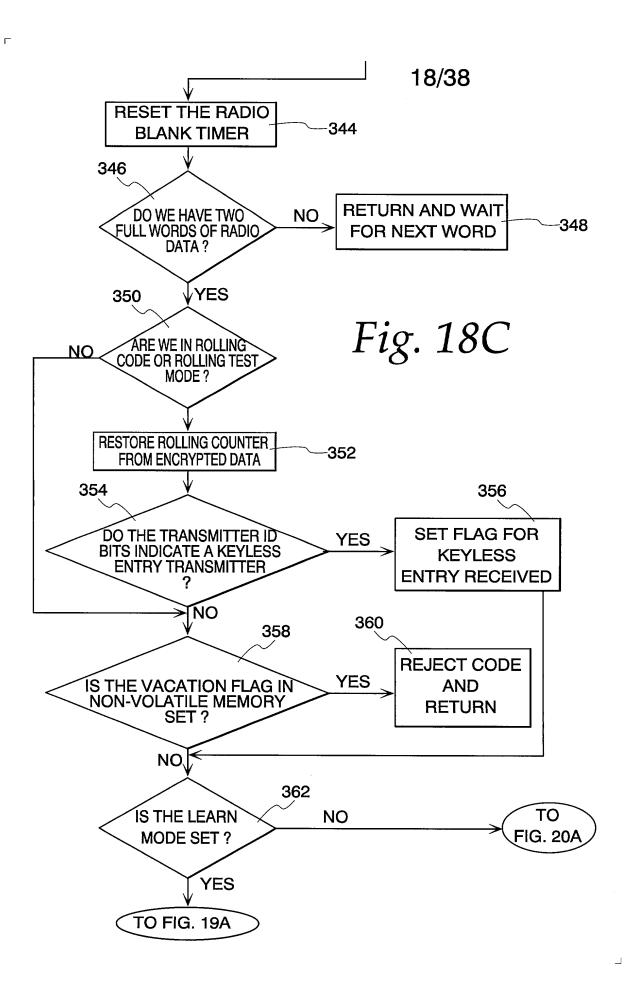
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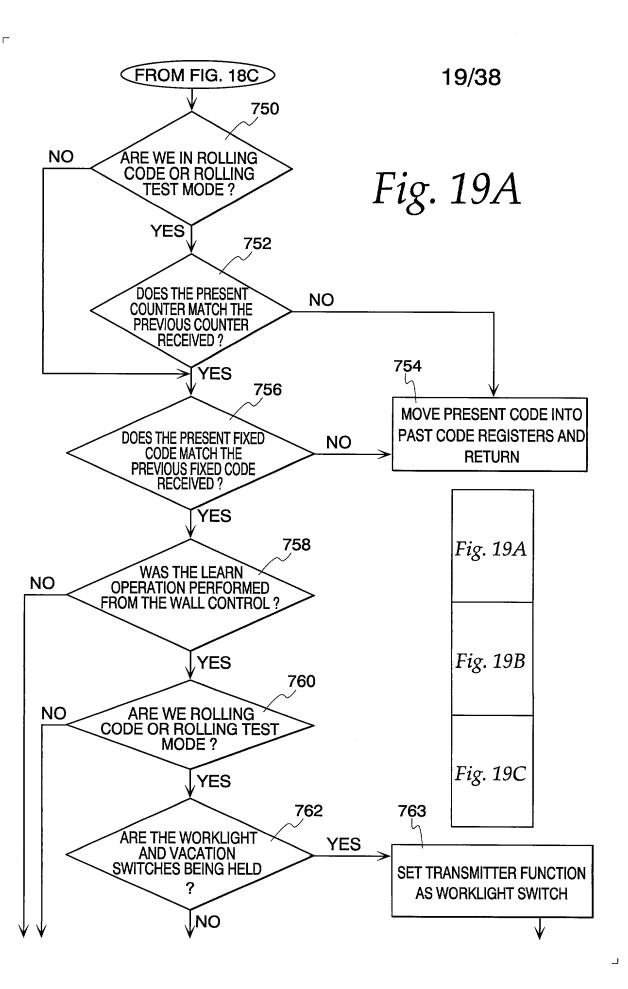


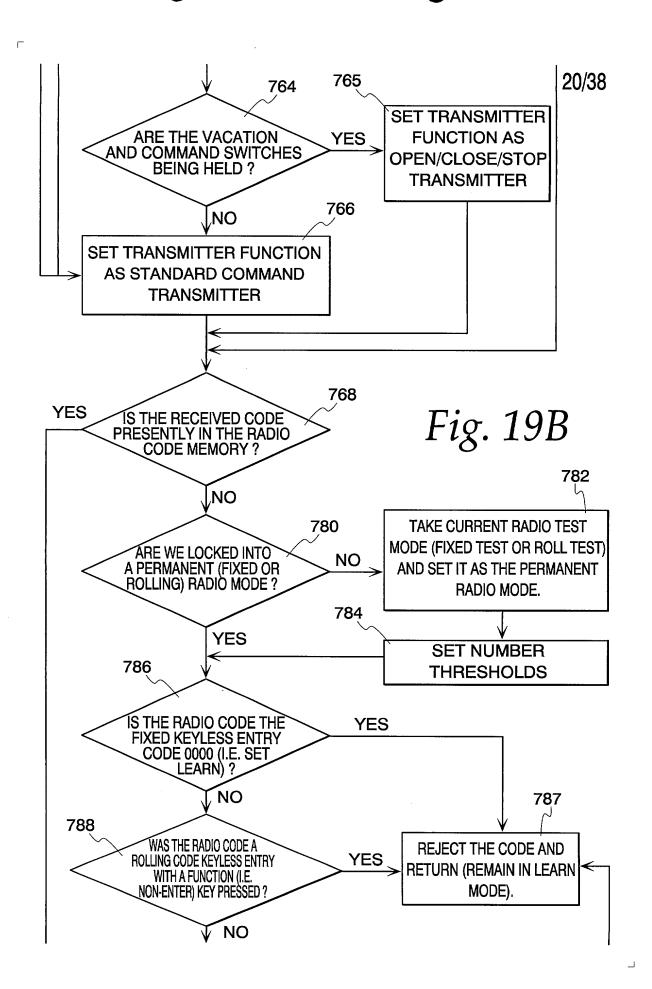


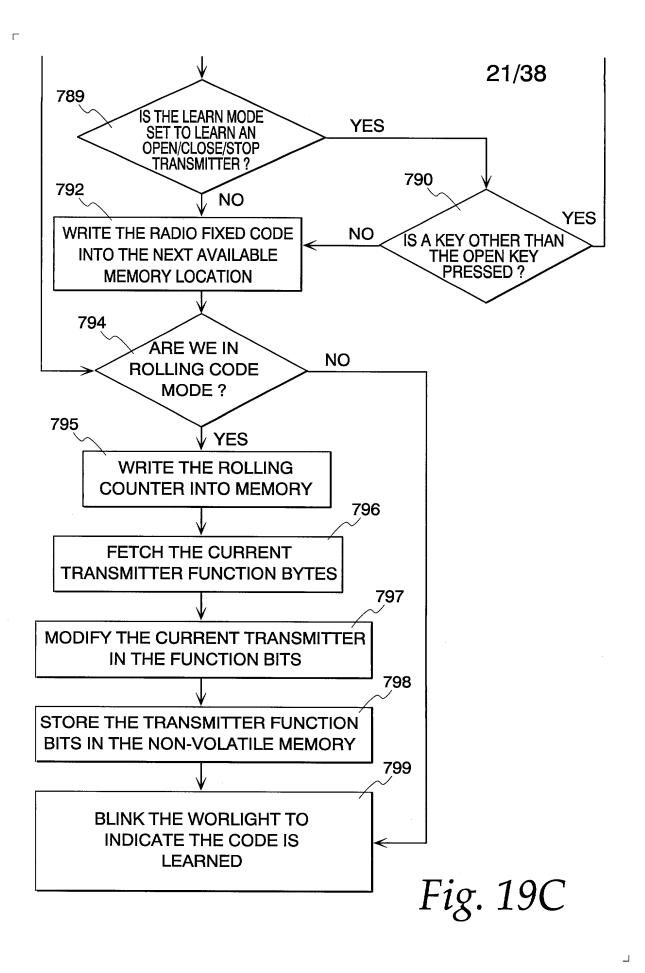


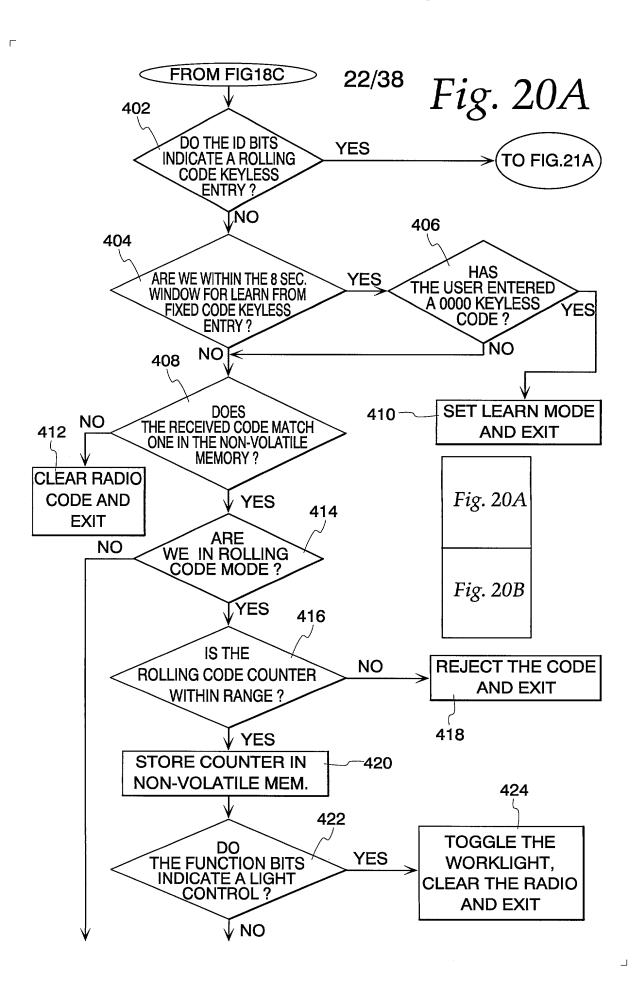


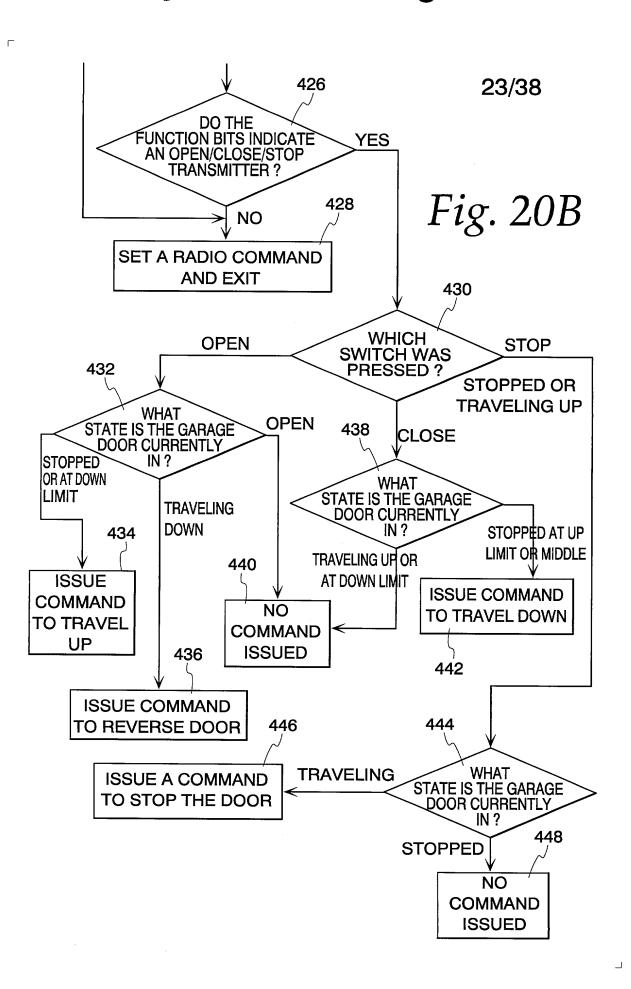


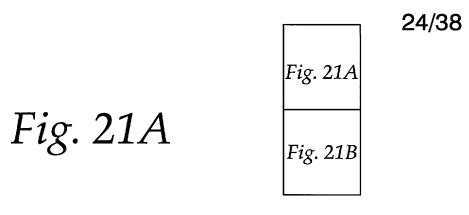


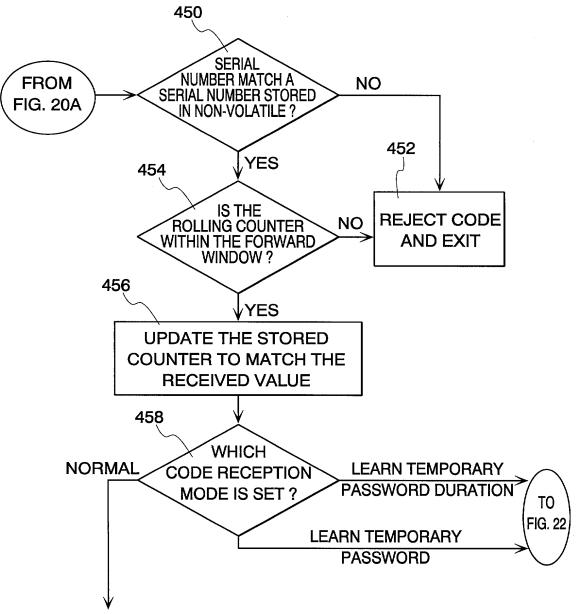




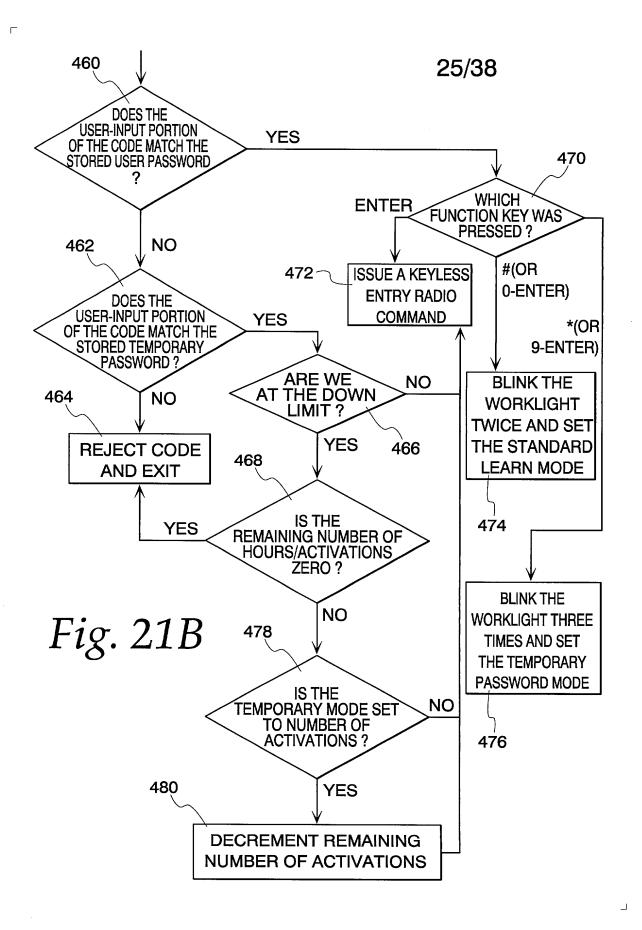


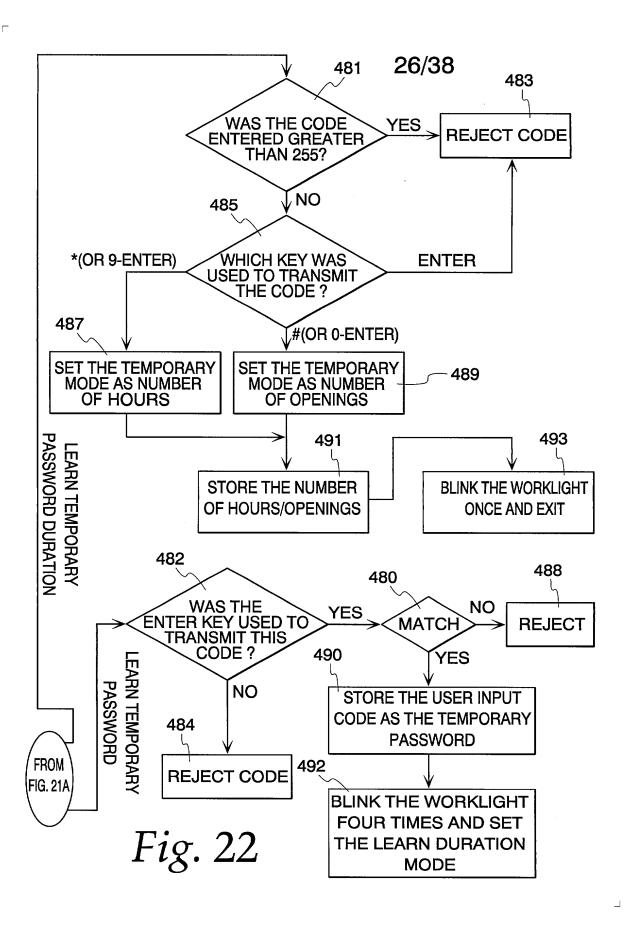


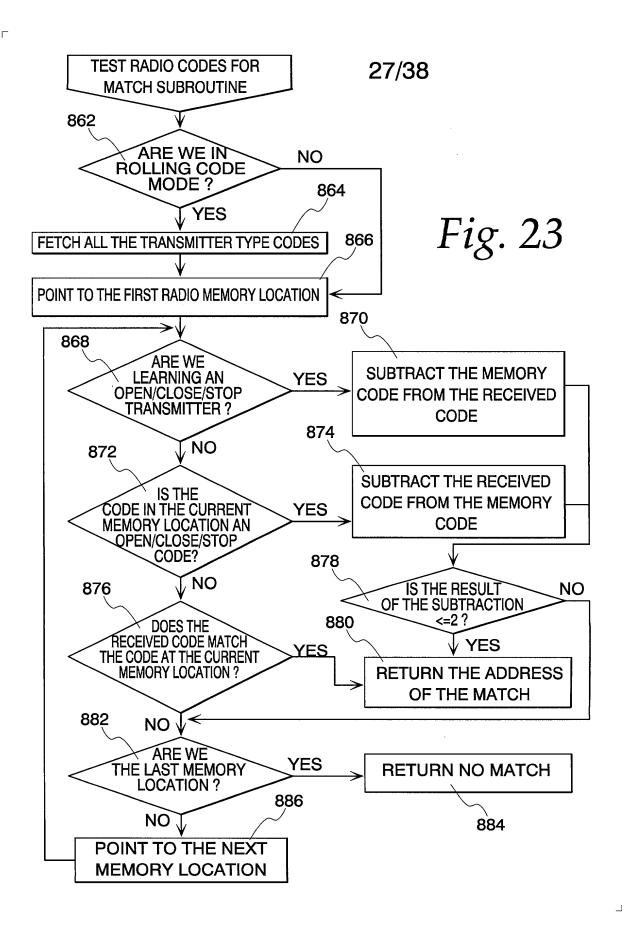


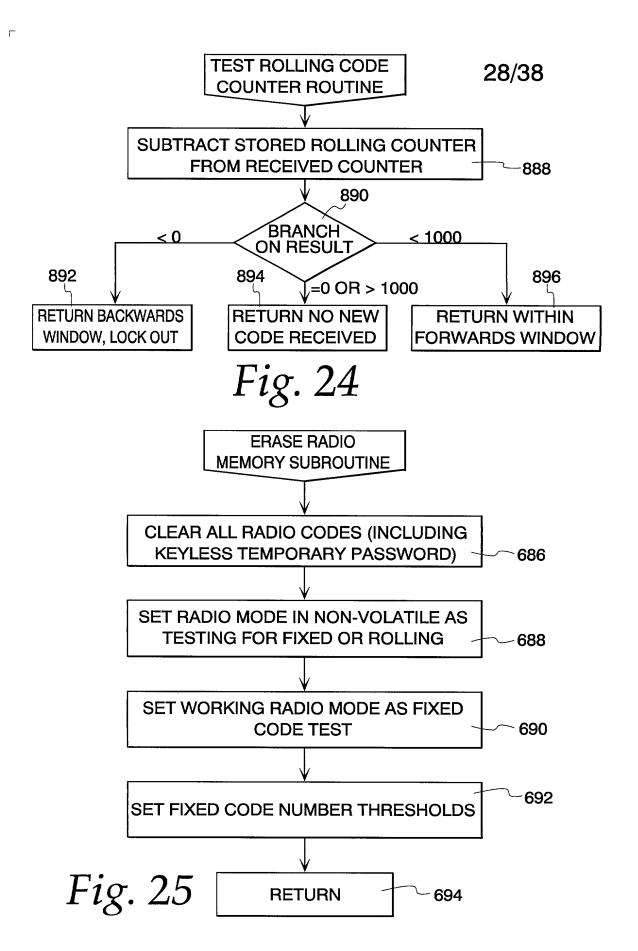


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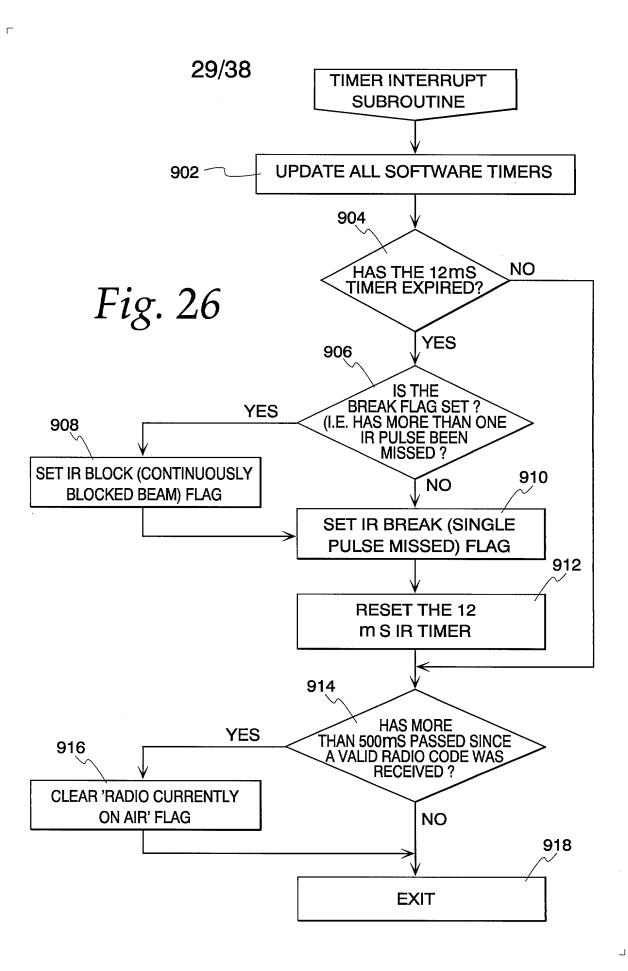








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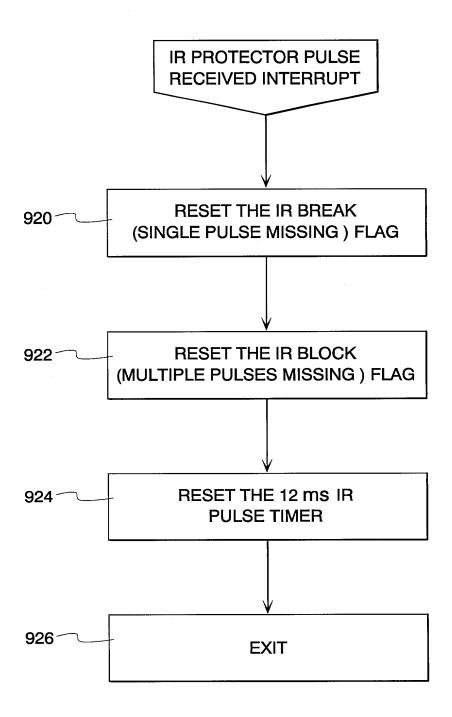
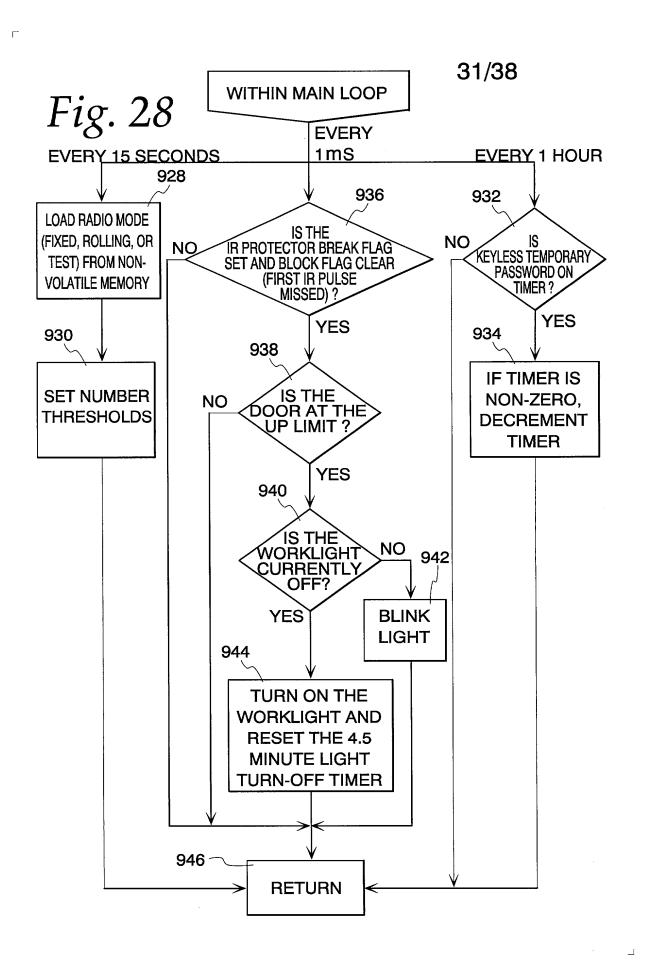
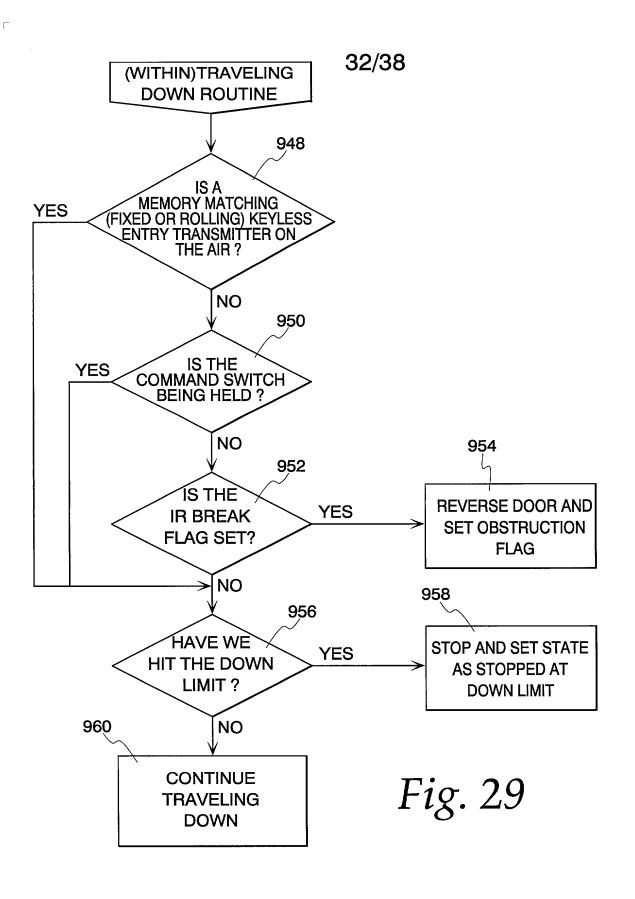
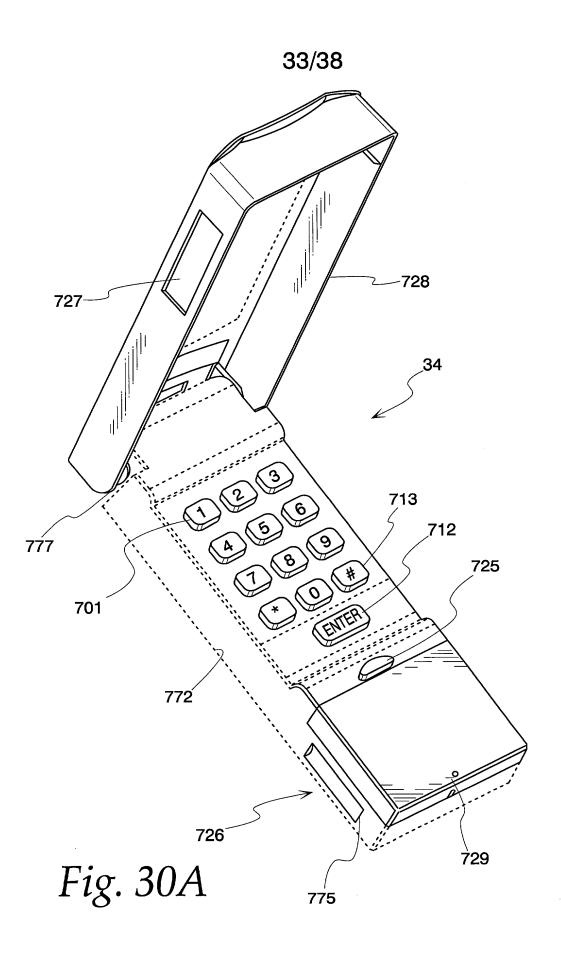


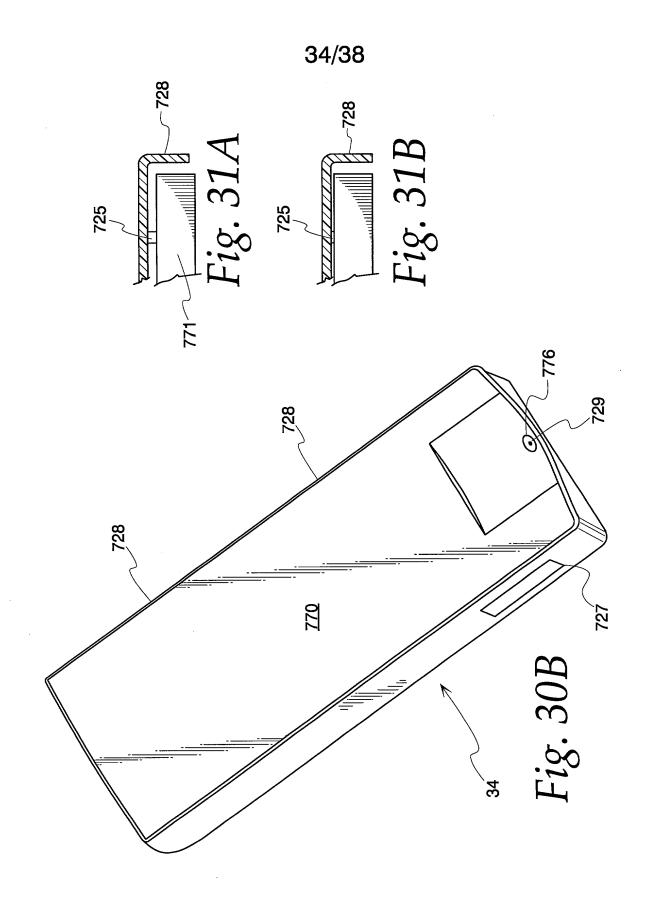
Fig. 27

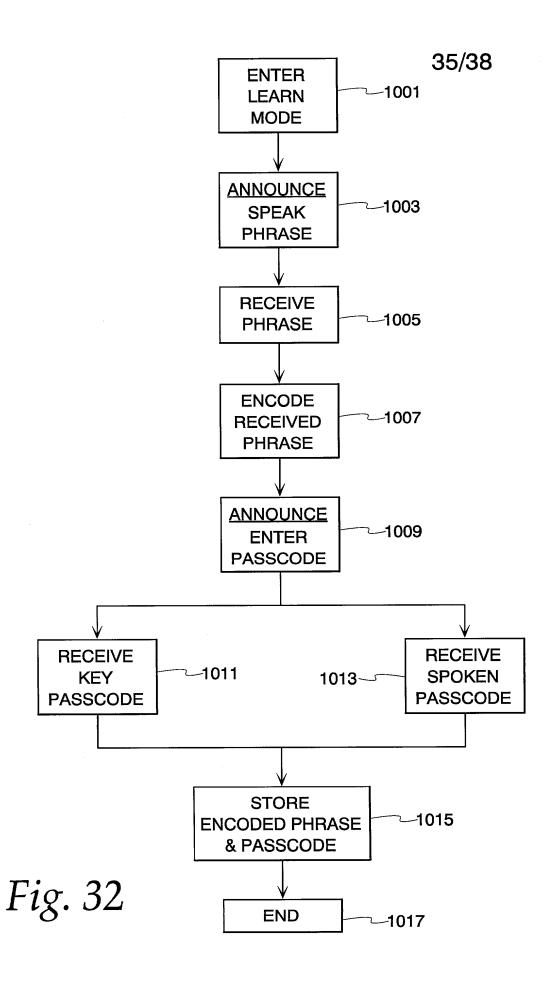
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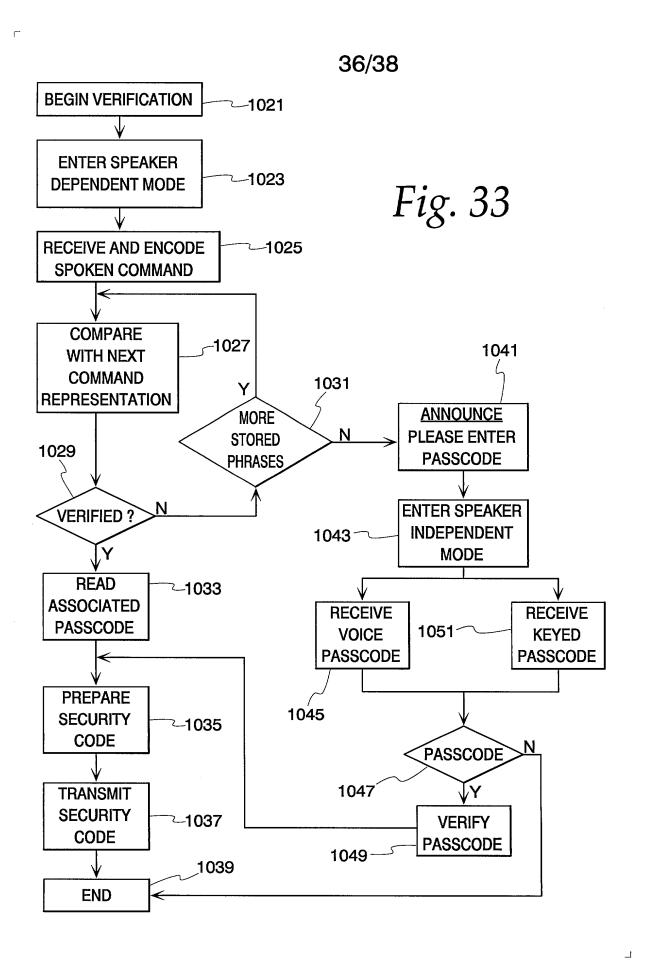


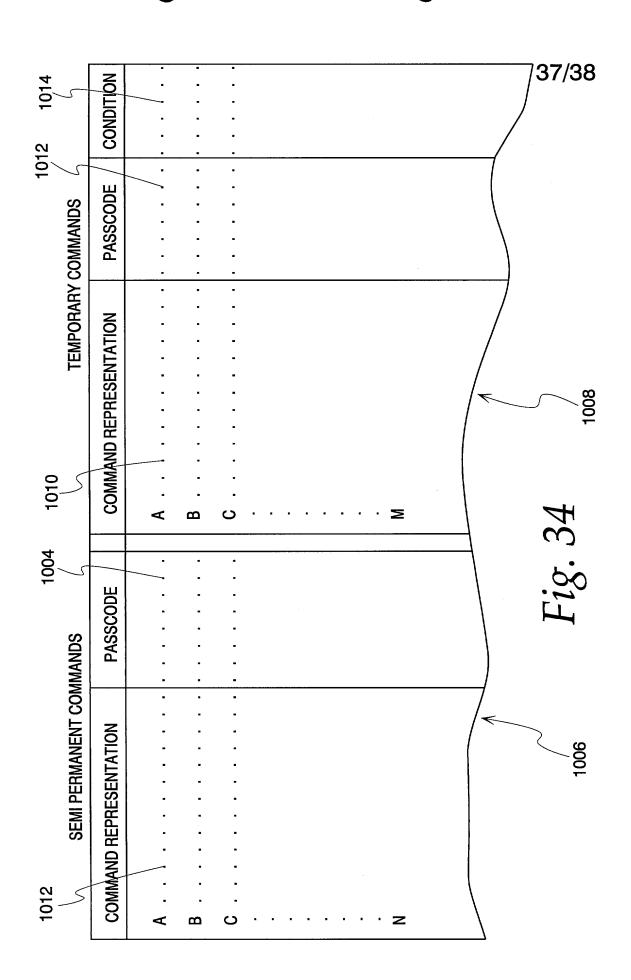












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